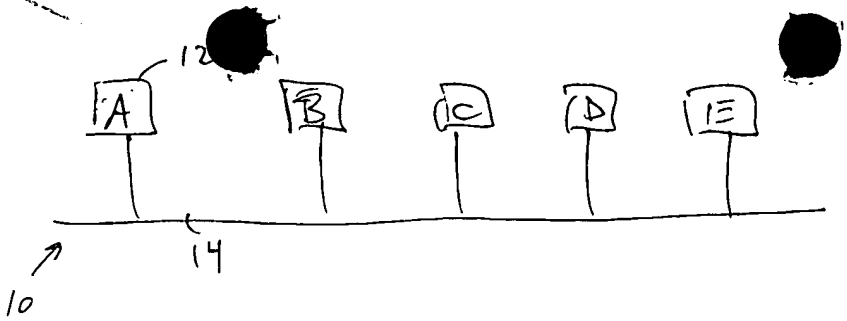

FIG 1A

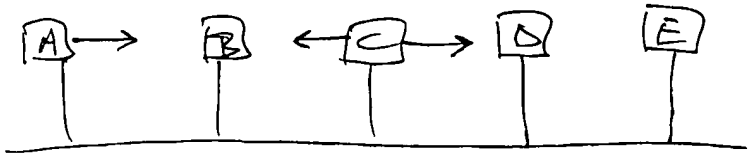

FIG 1B

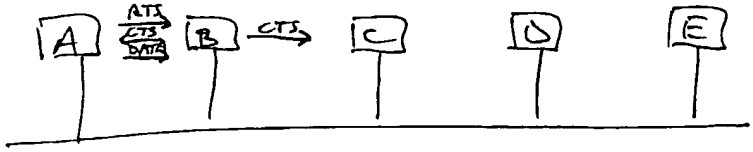

FIG 1C

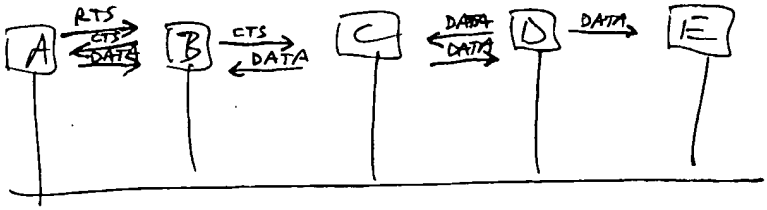

FIG 1D

0004004 · 0656950

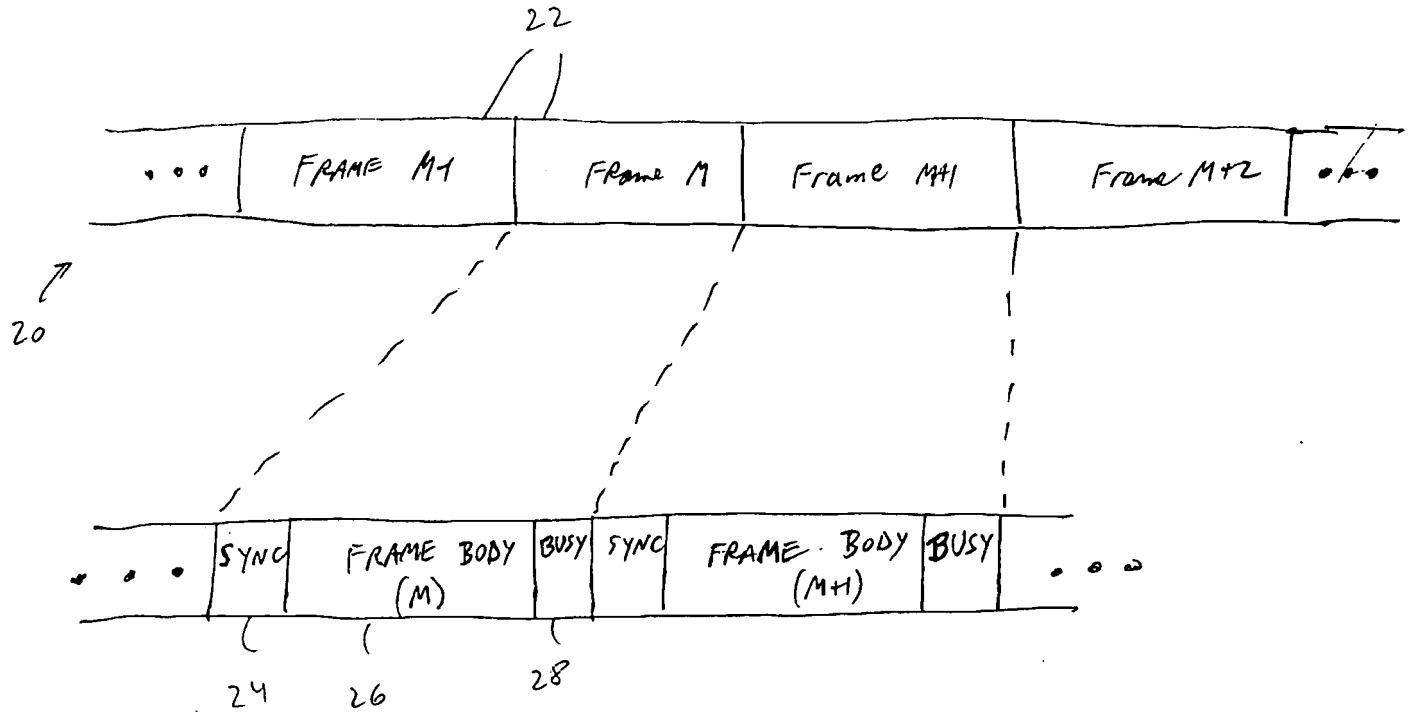
22
FRAME M-1
Frame M
Frame M+1
Frame M+2
20
SYNC
FRAME BODY (M)
BUSY
SYNC
FRAME BODY (M+1)
BUSY
24
26
28

FIG 2

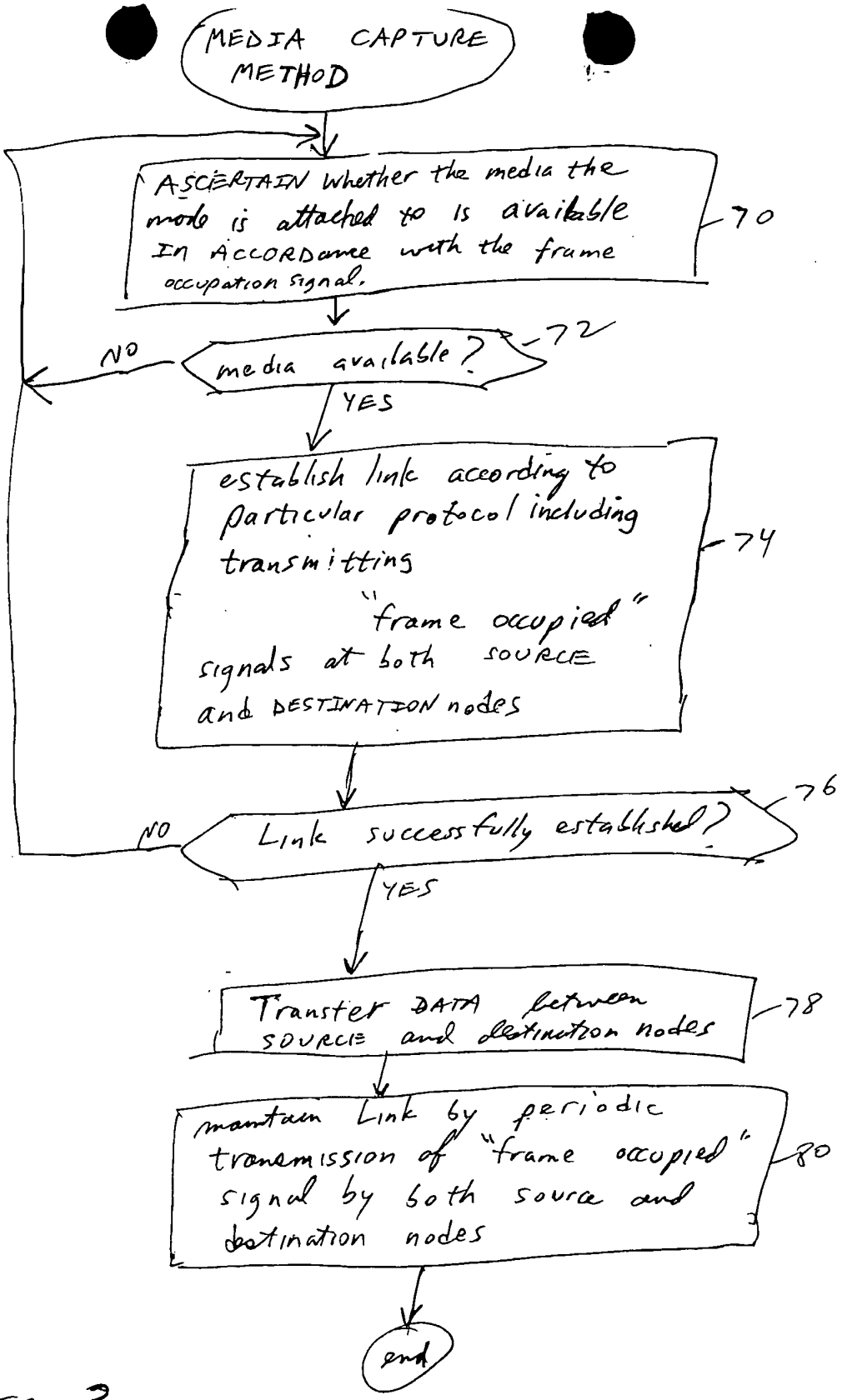

FIG 3

09/05/2003 10:45:05

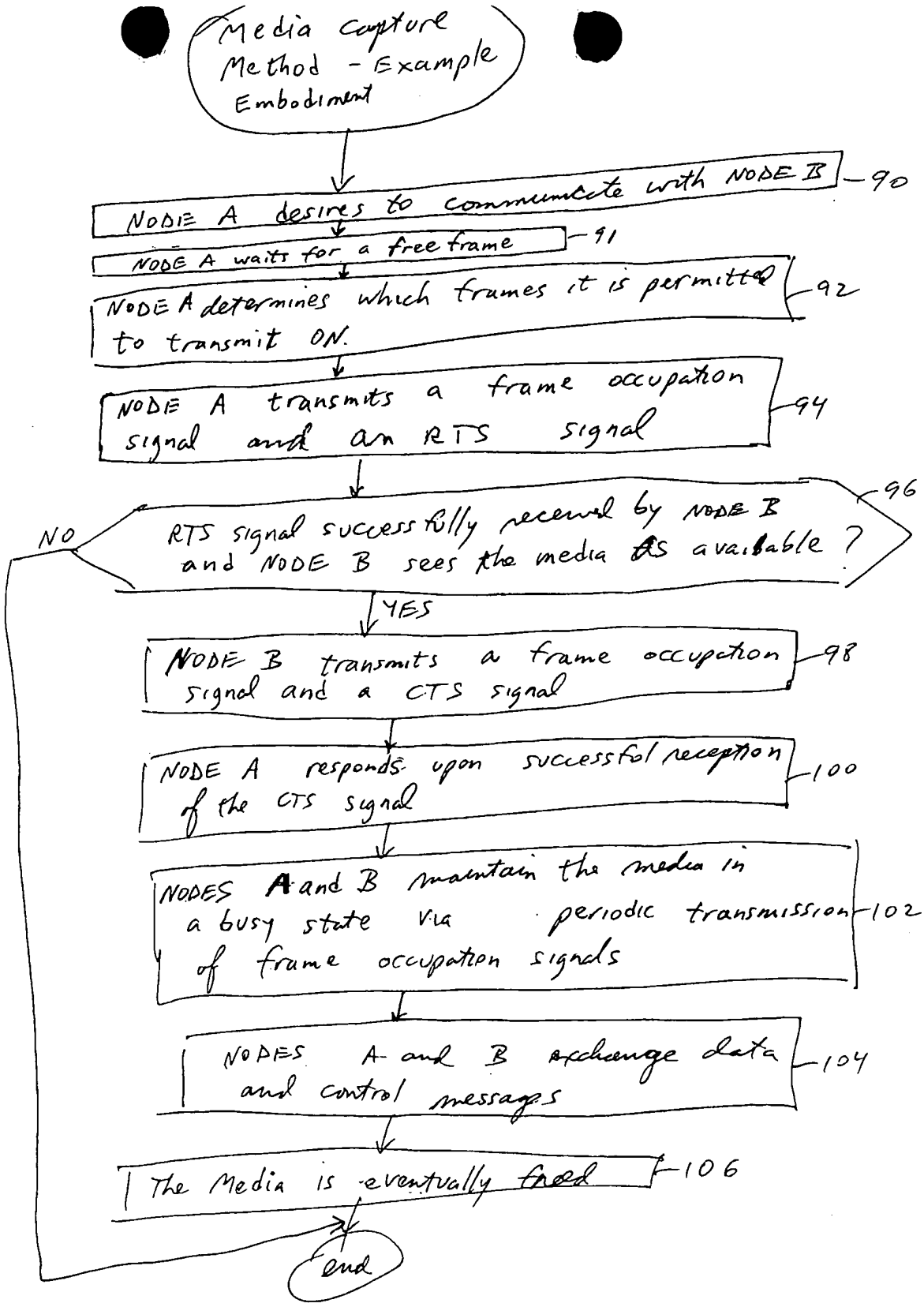

FIG 4

0054-0015-US-CON

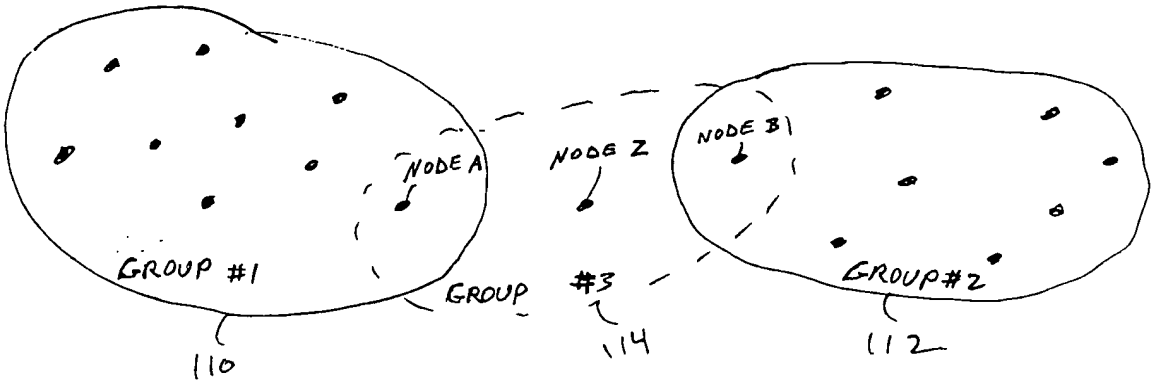

FIG 7

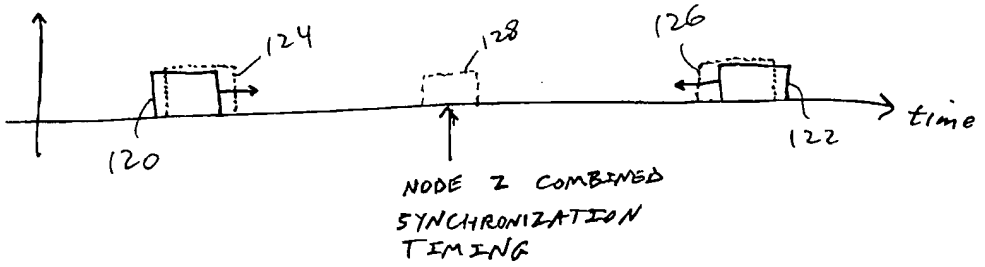

FIG 8

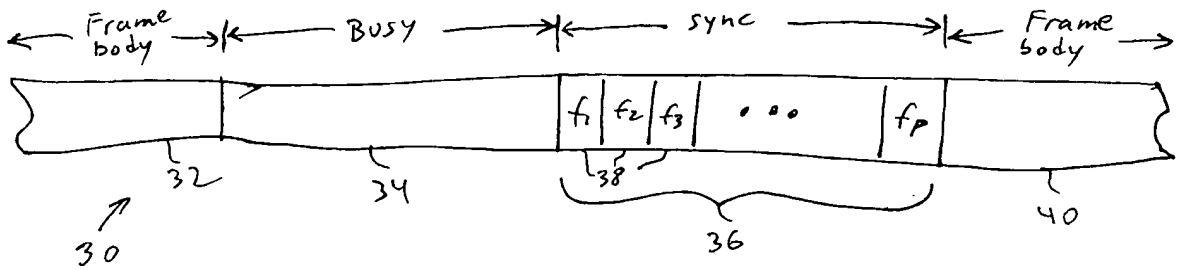

FIG 5

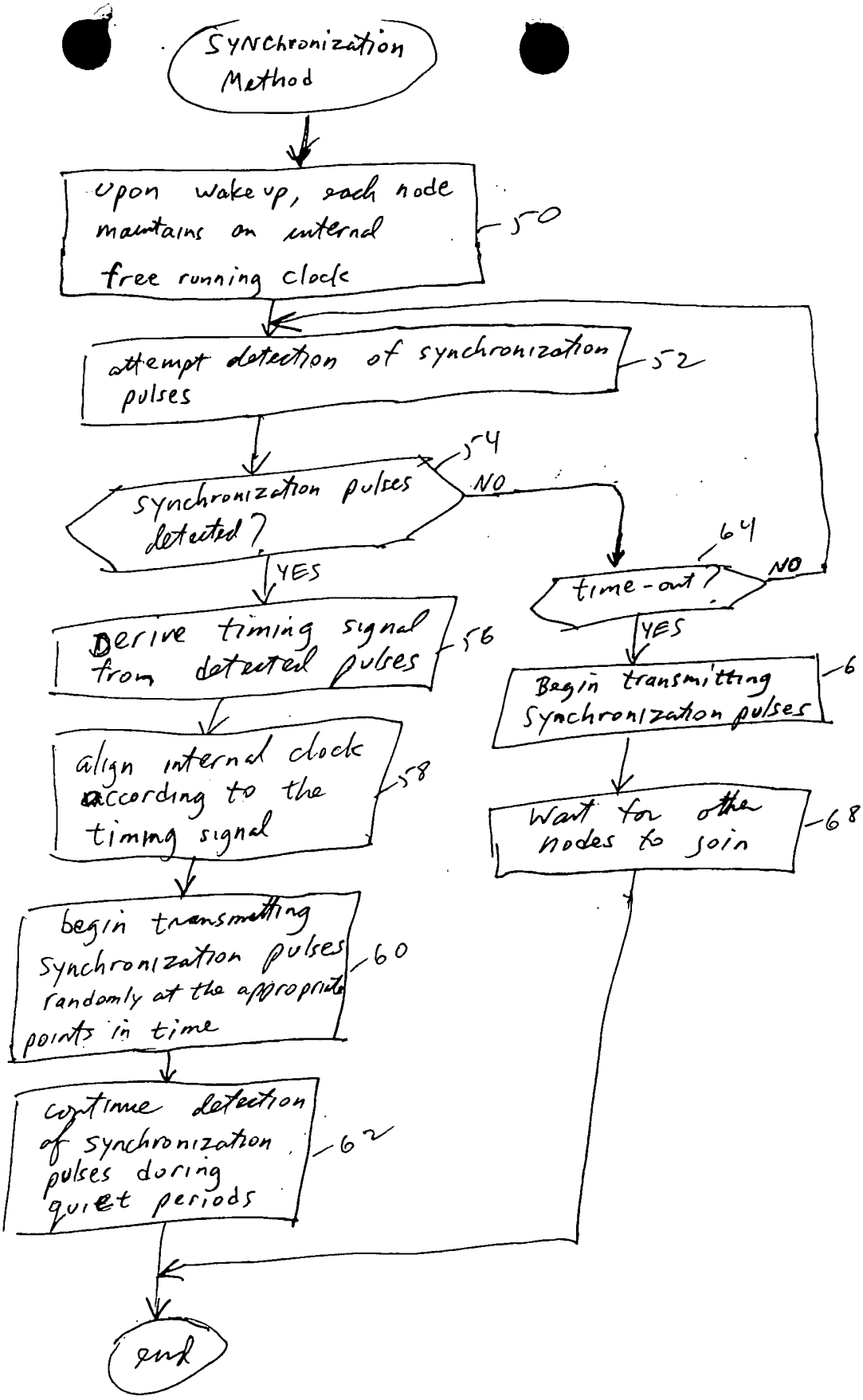
Synchronization Method
upon wake up, each node maintains an internal free running clock
50
attempt detection of synchronization pulses
52
synchronization pulses detected?
54
NO
YES
time-out?
64
NO
YES
Derive timing signal from detected pulses
56
align internal clock according to the timing signal
58
begin transmitting synchronization pulses randomly at the appropriate points in time
60
continue detection of synchronization pulses during quiet periods
62
Begin transmitting Synchronization pulses
66
Wait for other nodes to join
68
end

FIG 6

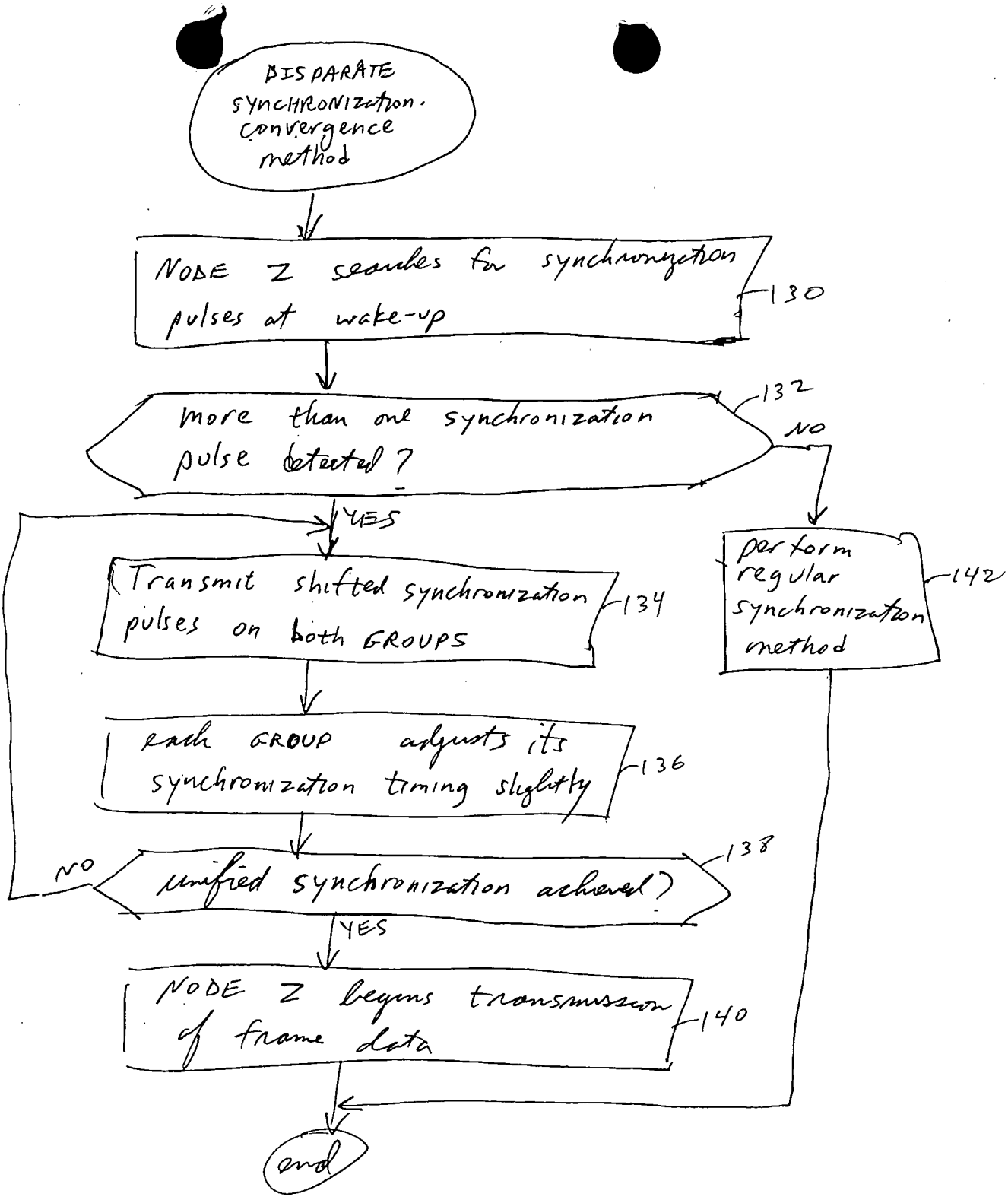
DISPARATE synchronization convergence method
NODE Z searches for synchronization pulses at wake-up
130
more than one synchronization pulse detected?
132
NO
YES
perform regular synchronization method
142
Transmit shifted synchronization pulses on both GROUPS
134
each GROUP adjusts its synchronization timing slightly
136
unified synchronization achieved?
138
NO
YES
NODE Z begins transmission of frame data
140
end

FIG 9

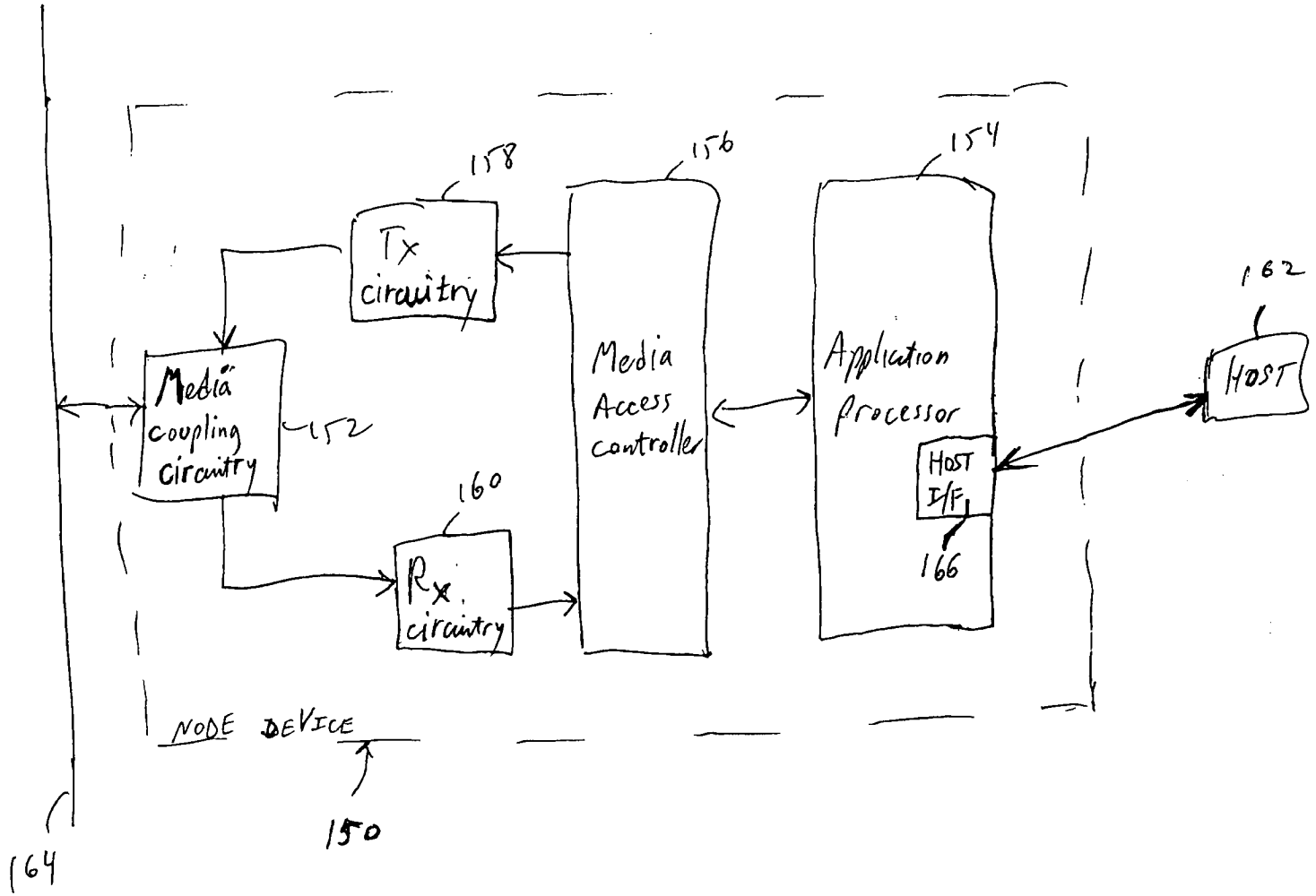
158
156
154
Tx circuitry
Media coupling circuitry
152
Media Access controller
Application Processor
162
HOST
HOST I/F
166
160
Rx circuitry
NODE DEVICE
150
164

FIG 10

DO NOT DESTROY

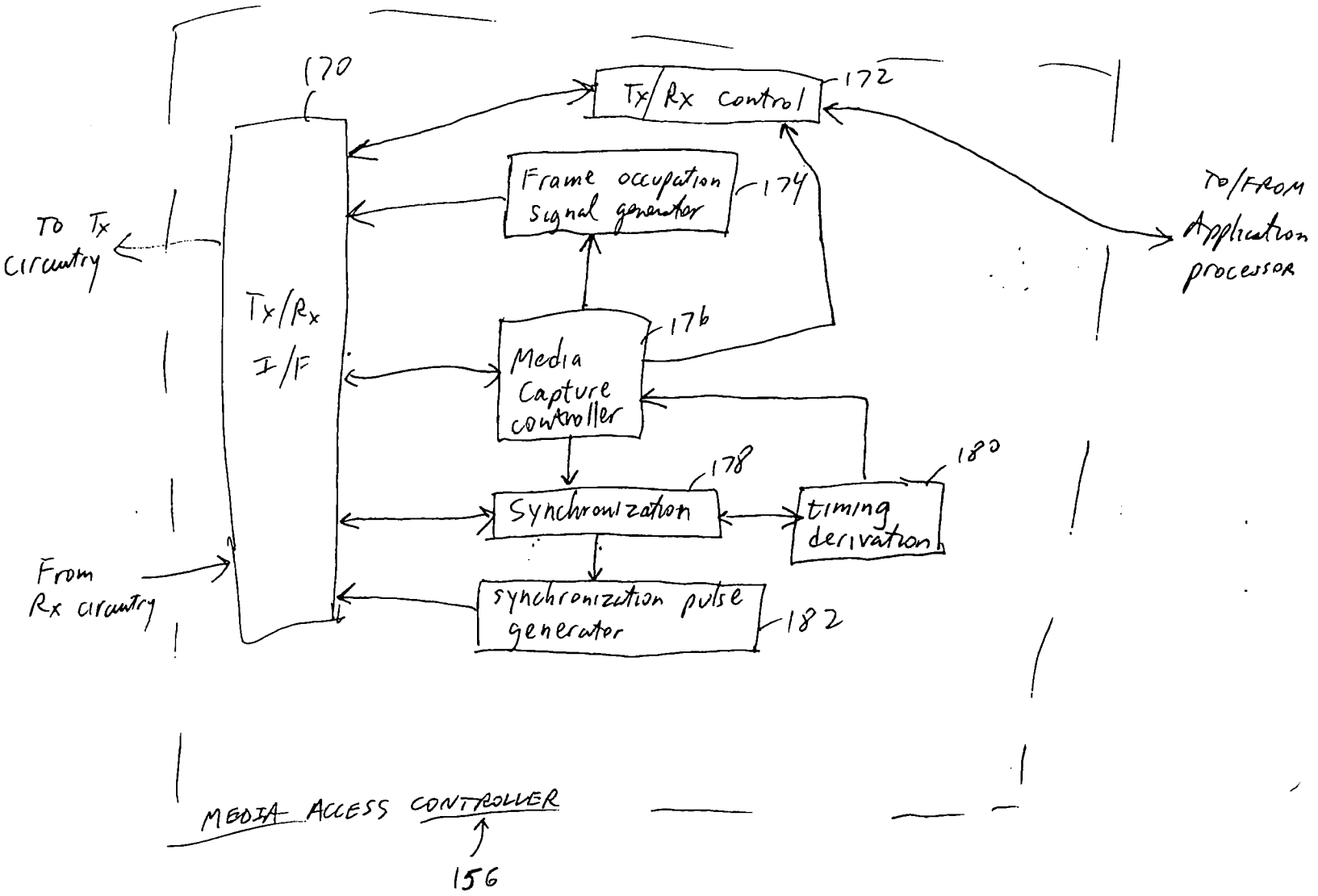

FIG 1